

## **Senior V&V Engineer**

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**Openings:** 1 (Los Altos, CA)

**Experience:** 5+ years

**Employment Type:** Full Time

### **Job Description:**

Looking for senior V&V Engineer who can take on leadership role in verification and validation of medical device products. Focus shall be on unit level and subsystem level verification of complex hardware, software, and fpga firmware for medical devices with large DSP content, sophisticated synchronization mechanisms as well as control logic for various sensors and interfaces. Previous experience in medical device V&V is a must.

### **Should have expertise in:**

- Medical device Verification and Validation
- Ability to create a test strategy and develop test cases from requirements at unit, subsystem and system level
- Ability to document Verification procedures and execute
- Manage distributed verification teams worldwide
- Ability to develop test cases and infrastructure in python, C/C++
- In depth knowledge of IEC62304 and ISO14971 standards
- White box and black box test experience

### **Good to have expertise in:**

- DSP algorithms verification
- FPGA firmware verification experience
- Software development background

## **V&V Engineer**

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**Openings:** 1 (Los Altos, CA), 2 (Vadodara, India), 2 (Bangalore, India)

**Experience:** 3+ years

**Employment Type:** Full Time

### **Job Description:**

Looking for verification and validation engineer in the medical device space. Engineers must have software development experience in order to be successful. Verification requires understanding of the overall system design and functionality of complex signal processing architectures. As an entry level position, this role is demanding and those who succeed demonstrate the ability to grasp system level concepts quickly and utilize them in their verification role. While specific domain knowledge is not required for this position they are nice to have.

### **Should have expertise in:**

- Python and/or C/C++ development

### **Good to have expertise in: (Need have have 2 of the following)**

- Previous medical device V&V experience
- DSP algorithm verification experience
- FPGA firmware verification experience
- C/C++ middleware/firmware development verification experience
- Analog/Digital hardware bringup, debug, and test experience